



PATENT
Docket No. 81788.0020

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#10/Ext of Time
T. Tran
5-6-00

In re application of:

Shota Kitamura, Seiji Yamada

Serial No: 09/392,865

Filed: September 9, 1999

For: Nonvolatile Semiconductor Memory
Device and Its Manufacturing Method

Art Unit: 2811

Examiner: T. Tran

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to:
Assistant Commissioner for Patents
Washington D.C. 20231, on

April 27, 2000

Date of Deposit

William H. Wright

Name

Signature

4/27/2000

Date

EXTENSION OF TIME PETITION

Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

In accordance with 37 C.F.R. 1.136, Applicant respectfully petitions the Commissioner for a one-month extension of time extending to April 28, 2000, the period for response to the Office Action dated December 28, 2000. A check for \$110 is enclosed. The responsive paper(s) are attached.

Please charge any insufficiency or credit any overpayment to Deposit Account No. 50-1314. A copy of this petition is enclosed.

05/03/2000 Y61ZAW 00000002 09392865

01 FC:115

110.00 OP

Date: April 27, 2000

500 South Grand Avenue, Suite 1900
Los Angeles, California 90071
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Respectfully submitted,

HOGAN & HARTSON L.L.P.

By:

William H. Wright, Reg. No. 36,312
Attorney for Applicant(s)

RECEIVED

MAY 05 2000

TECHNOLOGY CENTER 2800